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120518 Paper Id:

B.TECH (SEM V) THEORY EXAMINATION 2019-20 **MICROPROCESSOR & ITS APPLICATIONS**

Time: 3 Hours

1.

Total Marks: 100

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S E C T I OAN

 $2 \times 1 = 20$

a.	Define following: (i)Nibble(ii)word
b.	Give the significance of SIM and RIM instruction in 8085.
c.	How the clock signal is generated in 8085?
d.	Define compiler or interpreter in programming languages.
e.	Draw flag register of 8085.
f.	What is stack memory ? Explain.
g.	What is meant by cross complier ?
h.	How many modes are present in 8255 PPI?
i.	What rare the general purpose registers in 8086 microprocessor?
j.	Write about types of addressing modes in 8086

SECTION B

2 Attempt any three of the following.

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Z .	Attempt any <i>mille</i> of the following.
a.	Draw and explain the internal architecture of 8259 interrupt controller. Also describe
	its initialization command words?
b.	Explain the following instructions of 8085 microprocessors
	a) POP PSW b) XTHL c) SPHL d) PUSH PSW e) CMP M
с.	List out mask able and non-mask able interrupt available in 8085. Discuss in detail.
d.	Explain Minimum Mode operation of 8086 microprocessor with block diagram.
e.	Explain the addressing capability of 8085 microprocessor .How the 20 bit memory is
	addressed.

SECTION C

Attempt any part of the following: 3. 10x1 = 10Explain the function formats of 8086 ?Also explain the function of special bits a. used in instruction format. b. Discuss the concept of segmented memory? What are its advantages? Attempt any one part of the following: 4. 10x1 = 10Explain assembler level programming and draw the flowchart of assembler level a. programming? Write a program based on 8086 instruction set to compute addition of 16 bytes b. stored in memory ? 5. Attempt any one part of the following: 10x1 = 10Discuss the mode of operation of 8253 program, internal time with its control a. format. Give the features and functional block diagram of 8237 DMA controller. b. Attempt any one part of the following: 6. 10x1 = 10Write an assembly level program to find square root of given number. a. Draw and explain the memory and I/O read cycle of 8085. b. 7. Attempt any one part of the following: 10x1 = 10Explain all data transfer instruction of 8086. a. Interface an 8255 with 8086 to work as an I/O port. b.

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10x3=30